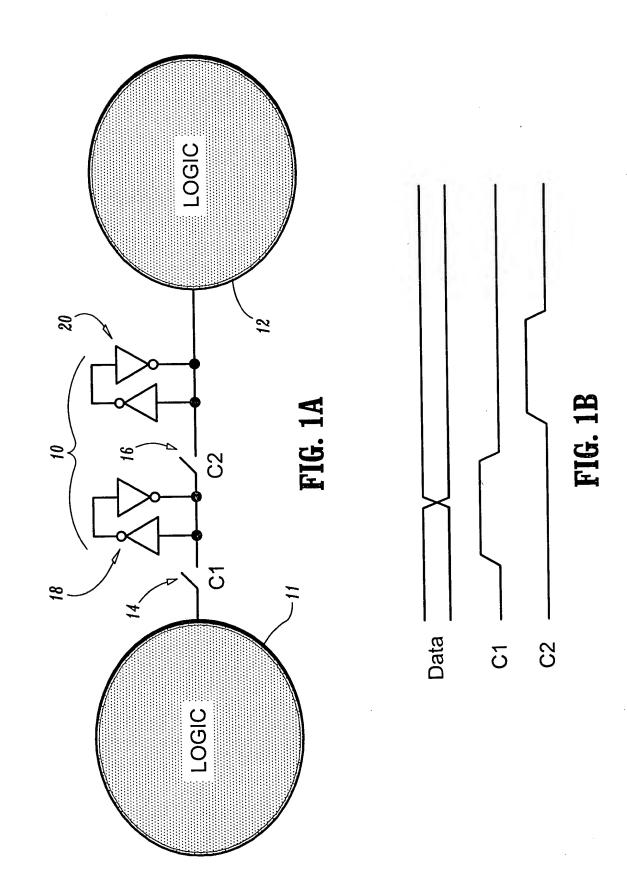
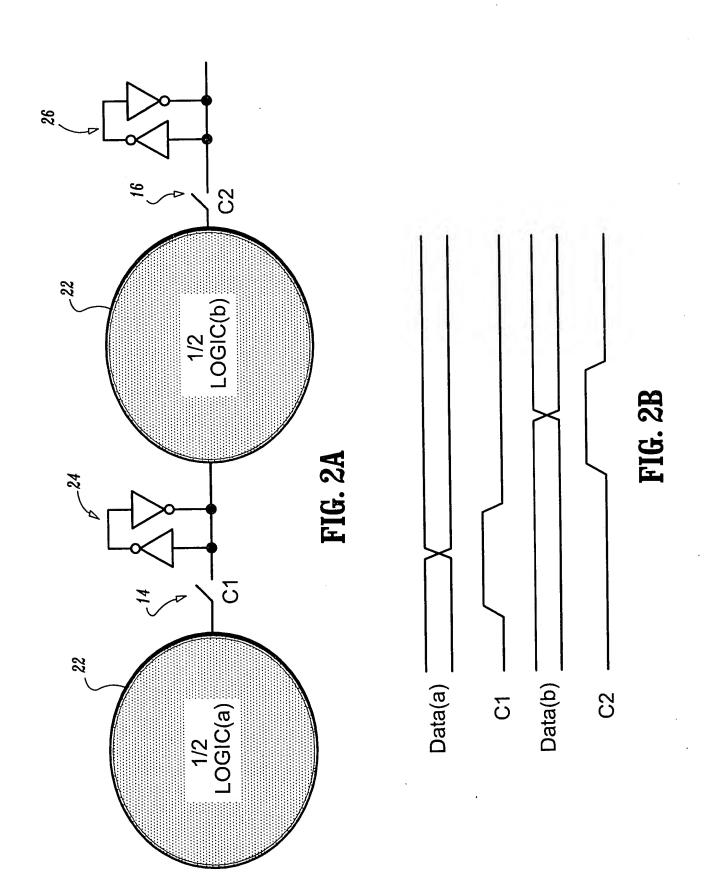
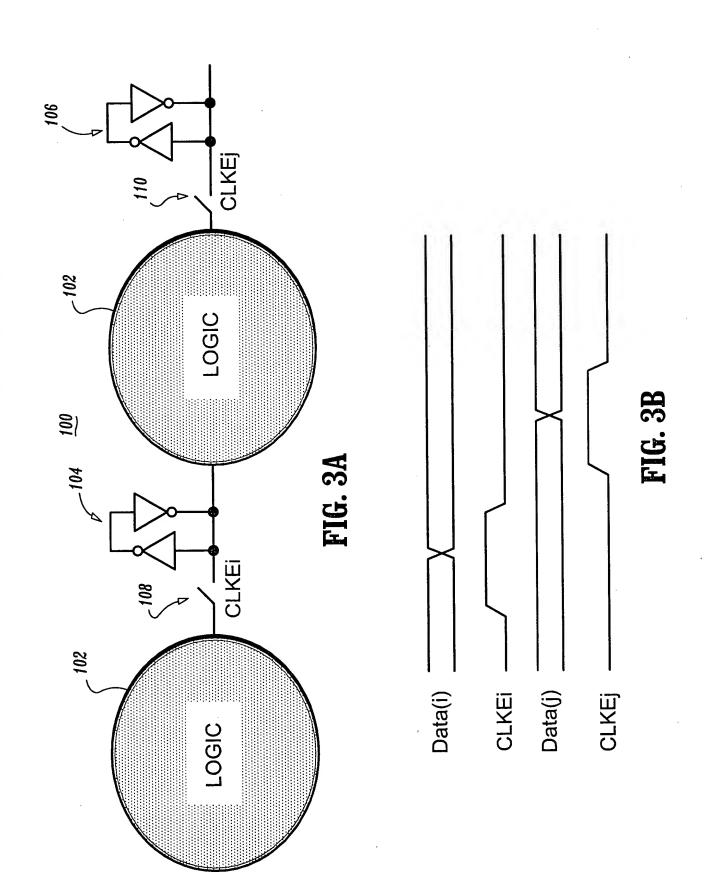
1/12 Peter Cook et al. YOR9-2000-0402 US1 (RPT) (8728-497)



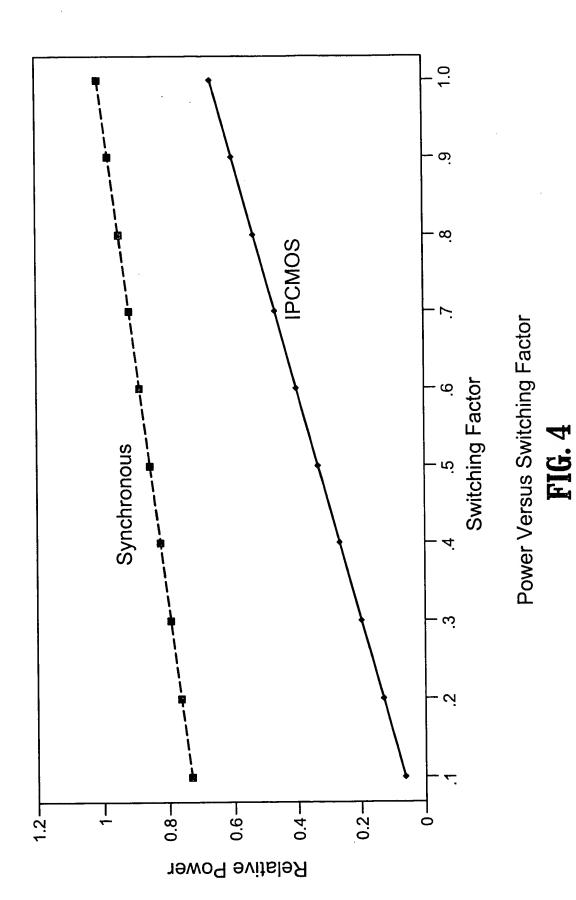
2/12 YOR9-2000-0402 US1 (8728-497)



3/12 YOR9-2000-0402 US1 (8728-497)



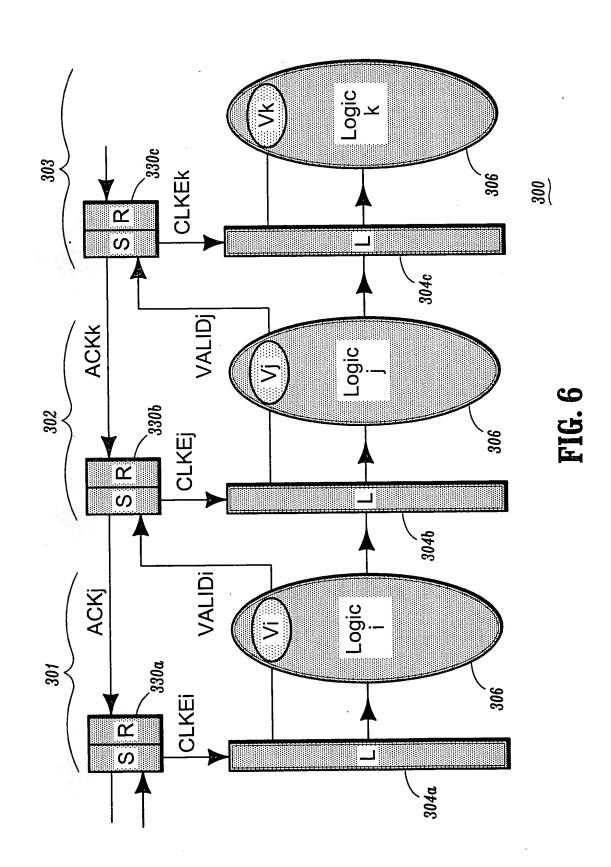
4/12 YOR9-2000-0402 US1 (8728-497)



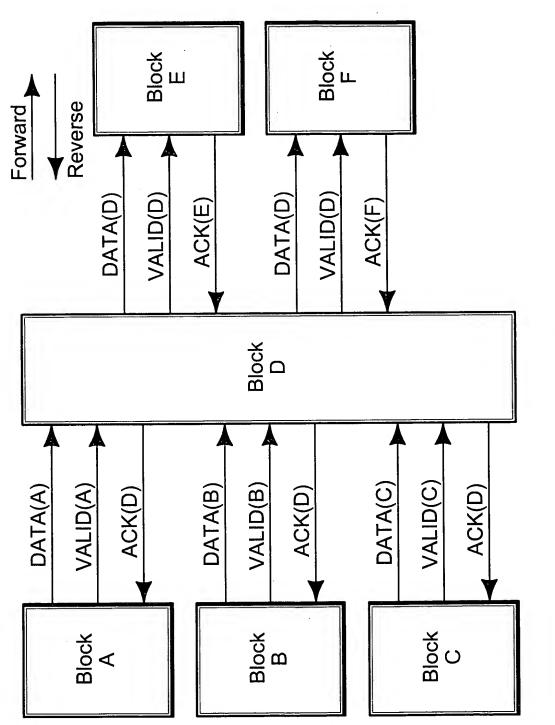
5/12 YOR9-2000-0402 US1 (8728-497) Local Clock STATIC LOGIC 213 VALIDI ACKj 204 Local Clock

FIG. 5

6/12 YOR9-2000-0402 US1 (8728-497)



7/12 YOR9-2000-0402 US1 (8728-497)



**FIG. 7** 

8/12 YOR9-2000-0402 US1 (8728-497)

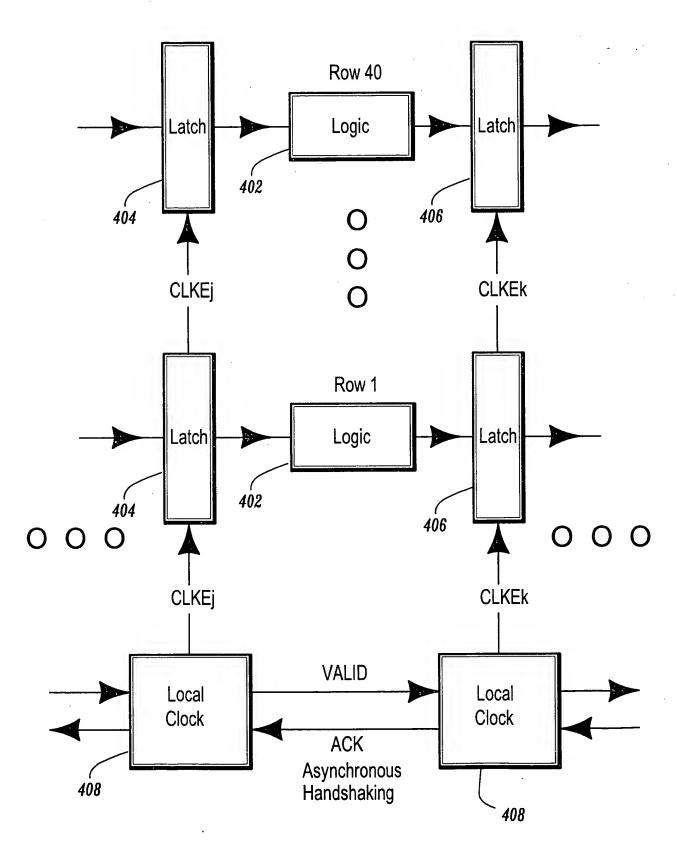
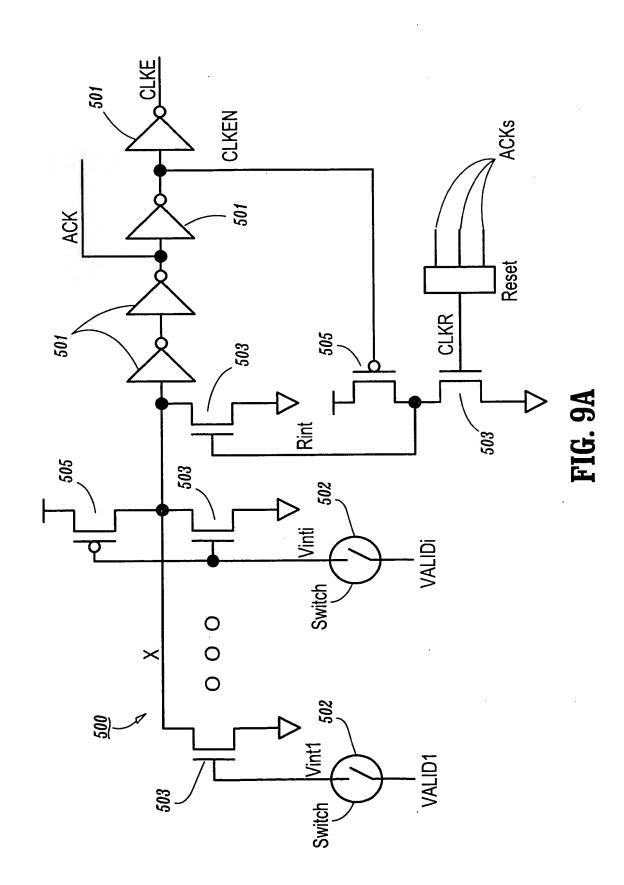
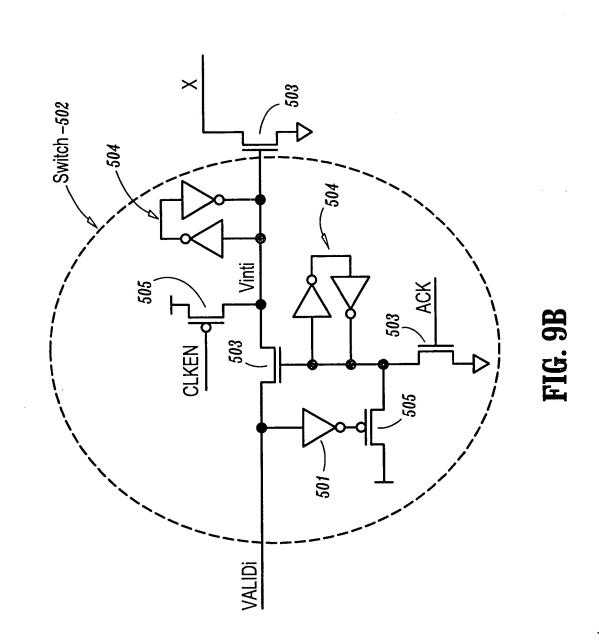


FIG. 8

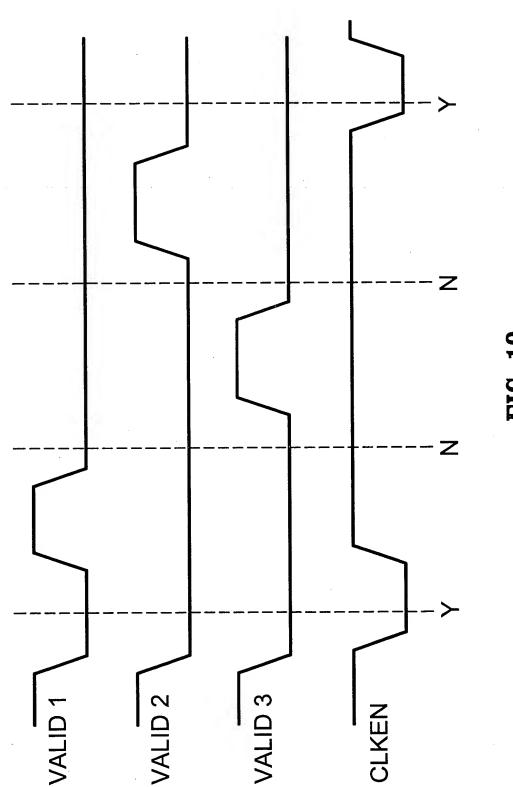
9/12 YOR9-2000-0402 US1 (8728-497)



10/12 YOR9-2000-0402 US1 (8728-497)



11/12 YOR9-2000-0402 US1 (8728-497)



']G. 10

12/12 YOR9-2000-0402 US1 (8728-497)

